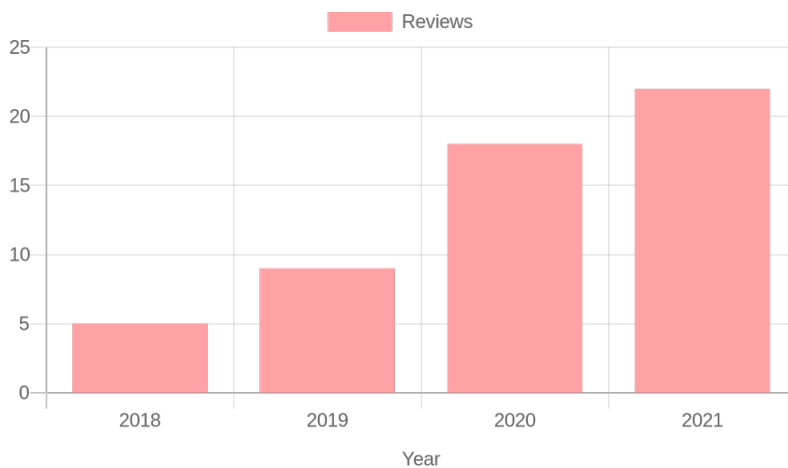# Biresh Kumar Joardar

https://publons.com/researcher/X-2732-2019/

Web of Science ResearcherID: X-2732-2019

Verified reviews

REVIEW SUMMARY

REVIEWER SUMMARY

For manuscripts reviewed from date range September 2016 - November 2021

(14) IEEE Transactions on Very Large Scal... **WOS**	(10) IEEE Transactions on Computer-Aided... **WOS**
(6) ACM Journal on Emerging Technologie... **WOS**	(5) IEEE Transactions on Computers **WOS**
(5) IEEE Design & Test **WOS**	(4) IEEE Embedded Systems Letters **WOS**
(3) IEEE Transactions on Automation Scien... **WOS**	(3) IEEE Journal on Emerging and Selected... **WOS**
(1) Transactions on Embedded Computing ... **WOS**	(1) IEEE/ACM Transactions on Computatio... **WOS**
(1) IEEE Transactions on Circuits and Syst... **WOS**	(1) Transactions on Architecture and Code ... **WOS**

54 REVIEWS OF 39 MANUSCRIPTS

From date range September 2016 - November 2021

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2 rounds from Aug 2021 to Nov 2021 for IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems

-

Reviewed: Nov 2021 for Transactions on Embedded Computing Systems

-

Reviewed: Nov 2021 for IEEE Transactions on Computers

- Reviewed: Nov 2021 for IEEE Transactions on Computers

- 3 rounds from Mar 2021 to Oct 2021 for IEEE Design & Test

- 3 rounds from Mar 2021 to Oct 2021 for IEEE Transactions on Automation Science and Engineering

- Reviewed: Aug 2021 for IEEE Transactions on Very Large Scale Integration (VLSI) Systems

- Reviewed: Aug 2021 for IEEE Design & Test

- 2 rounds from May 2021 to Jul 2021 for IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems

- Reviewed: Jun 2021 for IEEE/ACM Transactions on Computational Biology and Bioinformatics

- Reviewed: May 2021 for IEEE Transactions on Circuits and Systems II: Express Briefs

- 2 rounds from Mar 2021 to May 2021 for IEEE Transactions on Very Large Scale Integration (VLSI) Systems

- 2 rounds from Dec 2020 to Mar 2021 for IEEE Journal on Emerging and Selected Topics in Circuits and Systems

- Reviewed: Feb 2021 for IEEE Transactions on Very Large Scale Integration (VLSI) Systems

- Reviewed: Jan 2021 for ACM Journal on Emerging Technologies in Computing Systems

- Reviewed: Dec 2020 for IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems

- 2 rounds from Nov 2020 to Dec 2020 for ACM Journal on Emerging Technologies in Computing Systems

- Reviewed: Dec 2020 for Transactions on Architecture and Code Optimization

- Reviewed: Nov 2020 for ACM Journal on Emerging Technologies in Computing Systems

- 2 rounds from Oct 2020 to Nov 2020 for IEEE Embedded Systems Letters

- 2 rounds from Sep 2020 to Oct 2020 for IEEE Transactions on Very Large Scale Integration (VLSI) Systems

- Reviewed: Oct 2020 for IEEE Transactions on Computers

- Reviewed: Jul 2020 for IEEE Transactions on Very Large Scale Integration (VLSI) Systems

-

Reviewed: Jun 2020 for IEEE Journal on Emerging and Selected Topics in Circuits and Systems

-

Reviewed: May 2020 for IEEE Embedded Systems Letters

-

Reviewed: Apr 2020 for IEEE Transactions on Computers

-

Reviewed: Mar 2020 for IEEE Embedded Systems Letters

-

Reviewed: Mar 2020 for IEEE Transactions on Very Large Scale Integration (VLSI) Systems

-

Reviewed: Feb 2020 for IEEE Design & Test

-

2 rounds from Sep 2019 to Dec 2019 for ACM Journal on Emerging Technologies in Computing Systems

-

3 rounds from Aug 2019 to Dec 2019 for IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems

-

Reviewed: Nov 2019 for IEEE Transactions on Very Large Scale Integration (VLSI) Systems

-

Reviewed: Oct 2019 for IEEE Transactions on Computers

-

Reviewed: Sep 2019 for IEEE Transactions on Very Large Scale Integration (VLSI) Systems

-

2 rounds from Dec 2018 to Jan 2019 for IEEE Transactions on Very Large Scale Integration (VLSI) Systems

-

Reviewed: Nov 2018 for IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems

-

Reviewed: Sep 2018 for IEEE Transactions on Very Large Scale Integration (VLSI) Systems

-

Reviewed: Jul 2018 for IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems

-

Reviewed: Jul 2018 for IEEE Transactions on Very Large Scale Integration (VLSI) Systems

Peer review metrics

VERIFIED REVIEWS

54

Median: 3
96th percentile

VERIFIED REVIEWS (LAST 12 MONTHS)

27

Median: 1
99th percentile

REVIEW TO PUBLICATION RATIO

6.8:1

Median: 0.3:1